

Amendments to the Claims: This listing of claims will replace all prior versions, and listings, of claims in the application

Listing of Claims:

PATENT CLAIMS

We claim:

1. (Currently Amended) ~~Said A~~ voltage converter (~~SCW~~) for converting a said primary/secondary voltage (U_P/U_S) into a said-secondary/primary voltage (U_S/U_P), comprising:
 - at least one said-controlled switch (S_P/S_S), wherein
 - a said-control circuit (~~AST~~) that controls, according to its supplied set points, the at least one said-controlled switch (S_P, S_S) with a variable pulse duty factor and/or variable control times and/or variable frequency, and
 - characterized in that**
 - a said-digital signal processor (~~DSP~~) for the ~~running~~ calculation of the set points is ~~provided~~ for the said-control circuit (~~AST~~), and
 - wherein the said-voltage converter (~~SCW~~) comprises a said-(bus) interface (~~BSS~~), via which said-operating parameters (~~ppm~~) can be transmitted to the said-digital signal processor (~~DSP~~) and can be preset from a ~~said~~ an external control center (~~ELS~~).
2. (Currently Amended) ~~Said The~~ voltage converter (~~SCW~~) in accordance with claim 1, **characterized in that** wherein the said-(bus) interface (~~BSS~~) is bidirectional and said operating data (V_{PT}, U_{ST}, I_S) of the converter can be transmitted via the said-bus interface at the said-external control center (~~ELS~~).
3. (Currently Amended) ~~The voltage~~ Voltage-converter in accordance with claim 2, **characterized in that** further comprising a said-memory (~~SPE~~) is ~~provided~~ for the storage of operating data, which can be read out via the said-(bus) interface (~~BSS~~).
4. (Currently Amended) ~~The voltage~~ Voltage-converter in accordance with claim 2-~~or~~ 3, **characterized in that** further comprising a said-real time clock (~~RTC~~) is ~~provided~~ in order to correlate operating data with time values.
5. (Currently Amended) ~~Voltage The~~ voltage converter in accordance with ~~one of the~~ claims claim 2-through 4, **characterized in that** further comprising a ~~said~~ an auxiliary

energy memory (~~HES~~) is provided for the permanent energy supply of the said-digital signal processor (~~DSP~~) and/or of the said-real time clock (~~RTC~~).

6. (Currently Amended) ~~Voltage~~ The voltage converter in accordance with claim 5, ~~characterized in that~~ wherein the said-auxiliary energy memory (~~HES~~) is reloaded in the presence of primary voltage (~~U_p~~) and/or secondary voltage (~~U_s~~).
7. (New) The voltage converter in accordance with claim 3, further comprising a real time clock in order to correlate operating data with time values.
8. (New) The voltage converter in accordance with claim 3, further comprising an auxiliary energy memory for the permanent energy supply of the digital signal processor and/or of the real time clock.
9. (New) The voltage converter in accordance with claim 4, further comprising an auxiliary energy memory for the permanent energy supply of the digital signal processor and/or of the real time clock.
10. (New) The voltage converter in accordance with claim 8, wherein the auxiliary energy memory is reloaded in the presence of primary voltage and/or secondary voltage.
11. (New) The voltage converter in accordance with claim 9, wherein the auxiliary energy memory is reloaded in the presence of primary voltage and/or secondary voltage.

Amendment to the Abstract:

The Abstract has been amended. A revised Abstract is attached.

The invention relates to a voltage converter ~~(SCW)~~ for converting a primary/secondary voltage ~~(U_P/U_S)~~ into a secondary/primary voltage ~~(U_S/U_P)~~, comprising at least one controlled switch ~~(S_1 , S_3)~~, wherein a control circuit ~~(AST)~~ controls, according to its supplied set points, the at least one controlled switch ~~(S_1 , S_3)~~ with a variable pulse duty factor and/or variable control times and/or variable frequency. The invention ~~is characterized in that~~ further comprises a digital signal processor ~~(DSP)~~ for the running calculation of the set values ~~is provided for the control circuit (AST)~~, and the voltage converter ~~(SCW)~~ comprises a (bus) interface ~~(BSS)~~ via which operating parameters ~~(ppm)~~ can, from an external control center ~~(ELS)~~, be transmitted to the digital signal processor ~~(DSP)~~ and preset.

Fig. 1